

ABSTRACT

A method of power sequence protection for a  
level shifter is disclosed that includes the steps of  
5 placing the level shifter in a pre-selected state if an  
input voltage supply is not powered on when an output  
voltage supply is powered on and releasing the level  
shifter from the pre-selected state to follow transitions  
of an input signal when the input voltage supply is  
10 powered on.

402290 20233364